Rev. 3-21-01

DECLARATION AND

JAN 2 2 2002 Effective March 1998

OF ATTORNEY FOR U.S. PATENT APPLICATION

1	X) Origi	inal Λ	Supplemental	∩ Substitute	Λ	PCT	0 DESIGN
١	A) Ongi	uiai ()	Supplemental	() Substitute	v	101	() DESIGN

As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

Title: METHOD AND APPARATUS FOR CORRECTING INCLINATION OF IC ON SEMICONDUCTOR WAFER

of which is described and claimed in:	
() the attached specification, or	
(X) the specification in application Serial No.	, filed November 20, 2001, and with amendments through
, or	
() the specification in International Application No., fi	ed, and as amended on _(if applicable).

I hereby state that I have reviewed and understand the content of the above-identified specification, including the claims, as amended by any amendment(s) referred to above.

I acknowledge my duty to disclose to the Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

I hereby claim priority benefits under Title 35, United States Code, §119 (and §172 if this application is for a Design) of any application(s) for patent or inventor's certificate listed below and have also identified below any application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

COUNTRY	APPLICATION NO.	DATE OF FILING	PRIORITY CLAIMED	
Japan	2000-354471	November 21, 2000	YES	

I hereby claim the benefit under Title 35, United States Code §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code §112, I acknowledge the duty to disclose information material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

APPLICATION SERIAL NO.	U.S. FILING DATE	STATUS: PATENTED, PENDING, ABANDONED

And I hereby appoint Michael R. Davis, Reg. No. 25,134; Matthew M. Jacob, Reg. No. 25,154; Warren M. Cheek, Jr., Reg. No. 33,367; Nils Pedersen, Reg. No. 33,145; Charles R. Watts, Reg. No. 33,142; and Michael S. Huppert, Reg. No. 40,268, who together constitute the firm of WENDEROTH, LIND & PONACK, L.L.P., as well as any other attorneys and agents associated with Customer No. 000513, to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected therewith.

I hereby authorize the U.S. attorneys and agents named herein to accept and follow instructions from AOYAMA & PARTNERS as to any action to be taken in the U.S. Patent and Trademark Office regarding this application without direct communication between the U.S. attorneys and myself. In the event of a change in the persons from whom instructions may be taken, the U.S. attorneys named herein will be so notified by me.

Direct Correspondence to Customer No:



000313

PATENT TRADEMARK OFFICE

Direct Telephone Calls to:

WENDEROTH, LIND & PONACK, L.L.P. 2033 "K" Street, N.W., Suite 800 Washington, D.C. 20006-1021

Phone:(202) 721-8200 Fax:(202) 721-8250

		<u> </u>		
Full Name of First Inventor	FAMILY NAME NARITA	first given name Shoriki	SECOND GIVEN NAME	
Residence & Citizenship	сіту Hirakata-shi	state or country Japan	country of citizenship Japan	
Post Office Address	address 45-2-1104, Sakuraş	сіту gaoka-cho, Hirakatı	state or country zip code a-shi, Osaka-fu, Japan	
Full Name of Second Inventor	FAMILY NAME IKEYA	first given name Masahiko	SECOND GIVEN NAME	
Residence & Citizenship	сіту Sakai-shi	state or country Japan	country of citizenship Japan	
Post Office Address	ADDRESS 948-2-N102, Kanad	сіту oka-cho, Sakai-shi,	state or country zip code Osaka-fu, Japan	
Full Name of Third Inventor	FAMILY NAME TSUBOI	first given name Yasutaka	SECOND GIVEN NAME	
Residence & Citizenship	сіту Hirakata-shi	state or country Japan	country of citizenship Japan	· · · · · · · · · · · · · · · · · · ·
Post Office Address	address 3-17-33-301, Maki	сту nosaka, Hirakata-sl	state or country zip code ii, Osaka-fu, Japan	•
Full Name of Fourth Inventor	FAMILY NAME MAE	first given name Takaharu	SECOND GIVEN NAME	
Residence & Citizenship	сту Hirakata-shi	state or country Japan	country of citizenship Japan	
Post Office Address	address 45-1-1207, Sakurag	сіту gaoka-cho, Hirakata	state or country zip code a-shi, Osaka-fu, Japan	
Full Name of Fifth Inventor	FAMILY NAME KANAYAMA	first given name Shinji	SECOND GIVEN NAME	
Residence & Citizenship	сіту Kashihara-shi	state or country Japan	country of citizenship Japan	
Post Office Address	Address 3-11-1-102, Shirak	спу ashi-cho, Kashihara	state or country zip code -shi, Nara-ken, Japan	
Full Name of Sixth Inventor	FAMILY NAME	FIRST GIVEN NAME	SECOND GIVEN NAME	
Residence & Citizenship	СІТҮ	STATE OR COUNTRY	COUNTRY OF CITIZENSHIP	
Post Office Address	ADDRESS	СІТУ	STATE OR COUNTRY ZIP CODE	

I further declare that all statements made herein of my own knowledge are true, and that all statements on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

1st Inventor	Shoriki narita		Date	11,12,2001
2nd Invento	shoriki NARITA r <u>Masahiko Ik</u> a	ω <i>α</i> Γ		11. 12.200/
3rd Inventor	Masahiko IKEYA Jasutaka Tsubod	<u>. </u>	Date	11, 12, 200/
4th Inventor	Tafaharu MA	_	Date	11.12.200/
5th Inventor	Takaharu MAE Shinji Kan	nayama 1	Date	11.12.200/
6th Inventor	Shinji KANAYAMA	V	Date	
The above a	pplication may be more particularly identifie	ed as follows:		
U.S. Applica	ation Serial No.	Filing Date November 20, 2	2001	
Applicant R	eference Number 534048 Atty Docket No.	2001 17184		

Title of Invention METHOD AND APPARATUS FOR CORRECTING INCLINATION OF IC ON SEMICONDUCTOR WAFER

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